

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 1.53(b) Divisional Application of: **TAKAHASHI, Koji**

Group Art Unit: **2826**

Examiner: **Fazli Erdem**

Filed: **August 26, 2003**

For. **SEMICONDUCTOR MEMORY CAPABLE OF BEING DRIVEN AT LOW VOLTAGE AND ITS MANUFACTURE METHOD**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

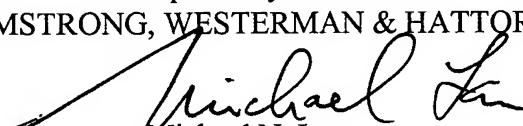
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 28, 2003

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/973,743.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,
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PATENT TRADEMARK OFFICE

Enclosure: PTO-1449

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INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 011118A	Serial No.
Applicant(s): TAKAHASHI, Koji		
	Filing Date: August 28, 2003	Group Art Unit: 2826

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,408,115	Chang	04/18/95	257	324	
_____	AB	5,969,383	Chang et al.	10/19/99	257	316	
_____	AC	5,838,041	Sakagami et al.	11/17/98	257	324	
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_____	AE	5,467,308	Chang et al.	11/14/95	365	185.01	

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AF	09-252059	09/22/97	Japan	Abstract
_____	AG				
_____	AH				
_____	AI				
_____	AJ				

OTHER DOCUMENTS

_____	AK	
_____	AL	
Examiner		Date Considered